Amendments to the Specification:

Please replace page 7, beginning at line 24 through page 8, line 2 of the specification with the following amended paragraph:

While various modules are transferred from ROM 108 and are unpacked when being stored in RAM 106, for purposes of illustration FIG. 2 shows an example of one module 212 being placed in RAM 106. The data module 212 has various portions that form a linked list in that pointer values stored in the module point to various locations of information within the module. In this example, the module 212 contains a main header portion 214 that contains a header signature portion 216 (e.g. MZ signature) and various offsets including a PE offset 218 indicating the location of the PE signature discussed above. The module 212 also includes a reallocation table 222 that maintains a set of offsets to the pointer values that point to the various locations of information including local variables and functions within the module 212 212.

Please replace page 8, beginning at line 25 through page 9, line 4 of the specification with the following amended paragraph:

Through execution of the program of the module, the pointer to the program point of entry of the module is decremented at decrement operation 306 by a set amount that is also dependent upon the particular processor to move from one boundary to a previous one. For example, the set amount to decrement is 8 bits for the Itanium® family of processors. Decrementing the pointer allows for searching of the main header for the module. Upon decrementing the pointer by the set amount, the module information at that location is read to determine if the main header is located at that position at read operation 308 308. Query operation 310 detects whether the main header has been found. Several pieces of information may be sought when reading from the module to find the main header, including the MZ signature, the PE signature, the machine type, and the image type.